

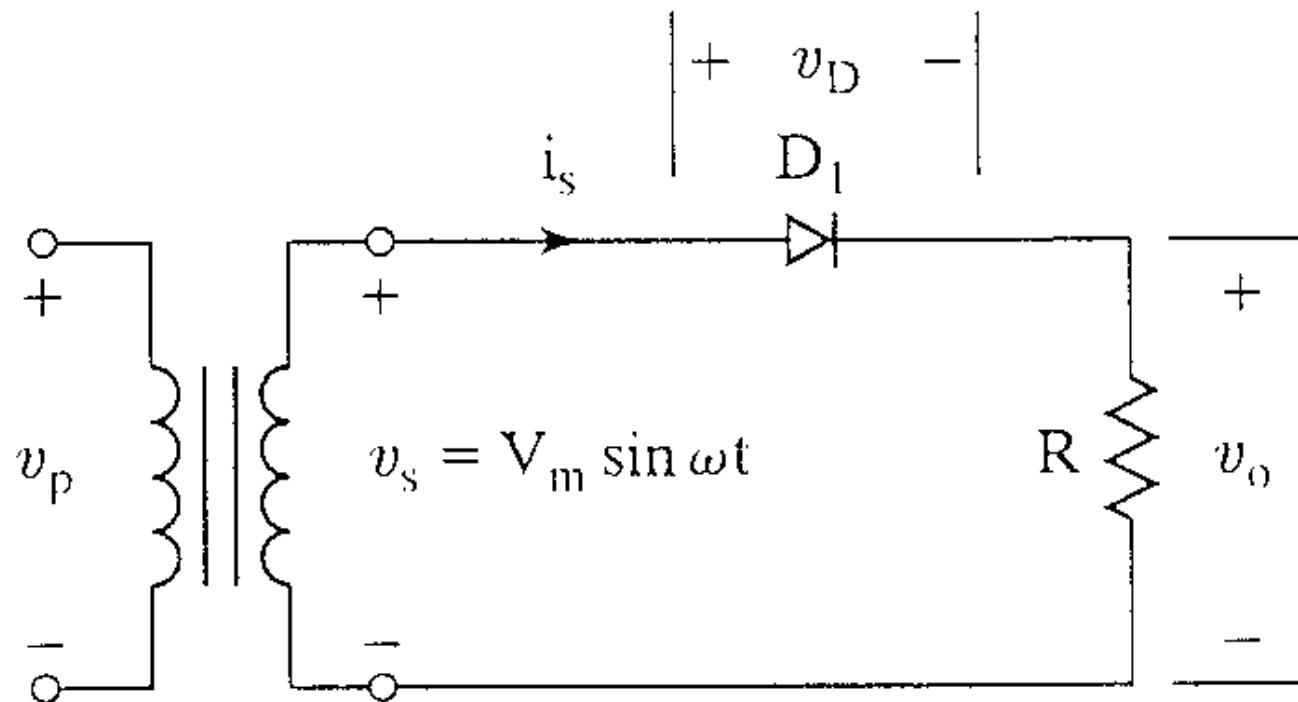
# Rectification

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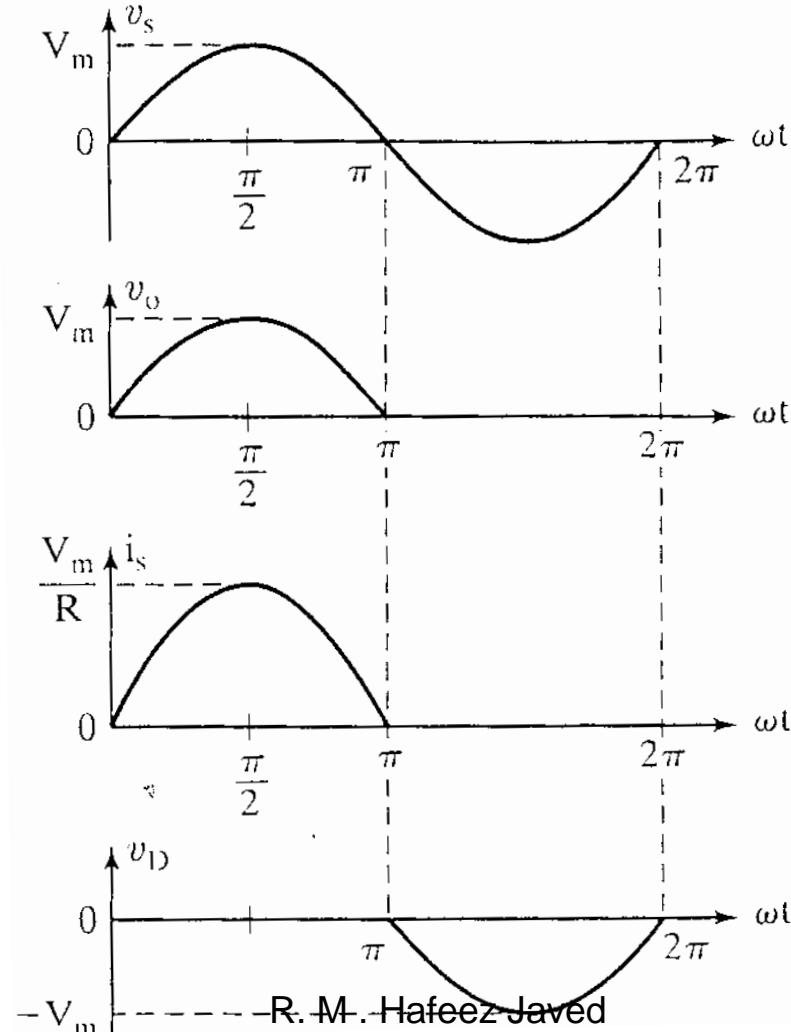
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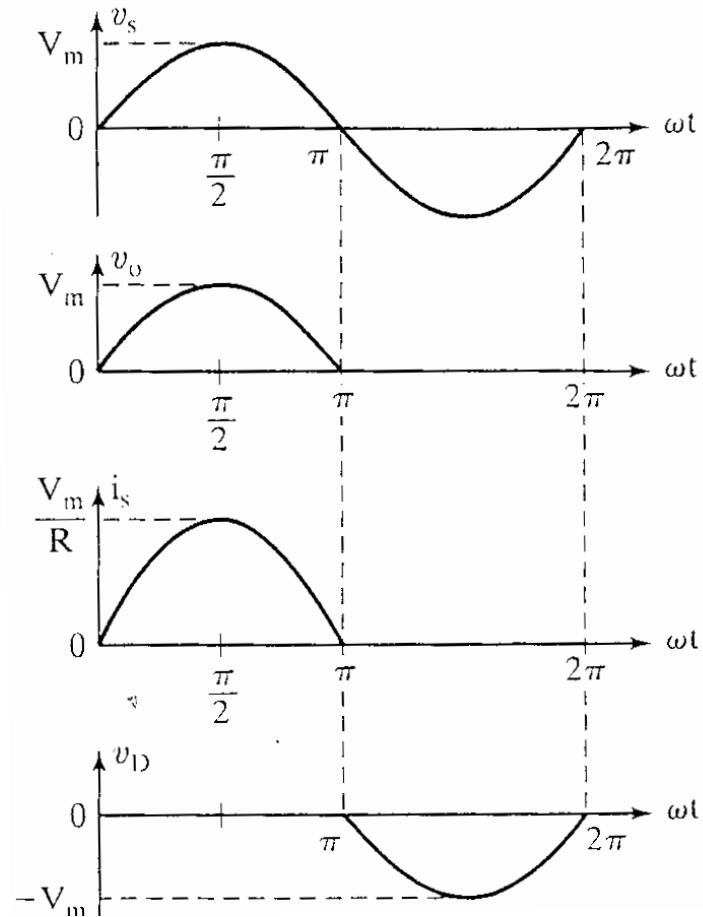
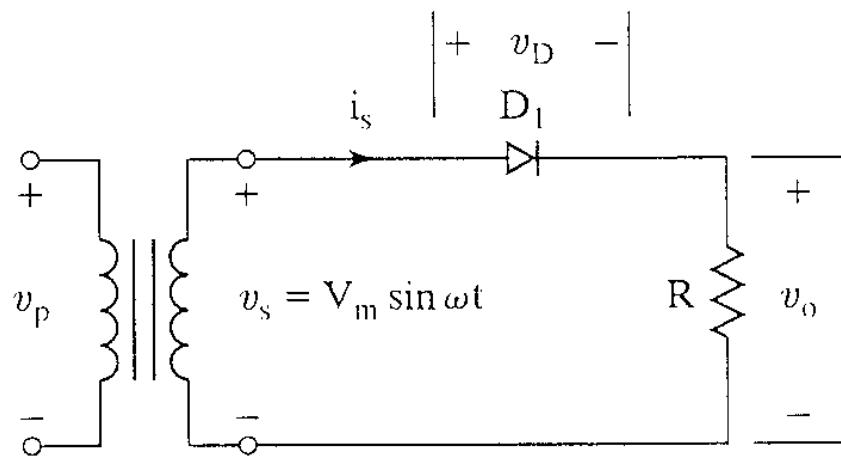
# Single-Phase Half-Wave Rectifier



# Waveforms



# Single-Phase Half-Wave Rectifier

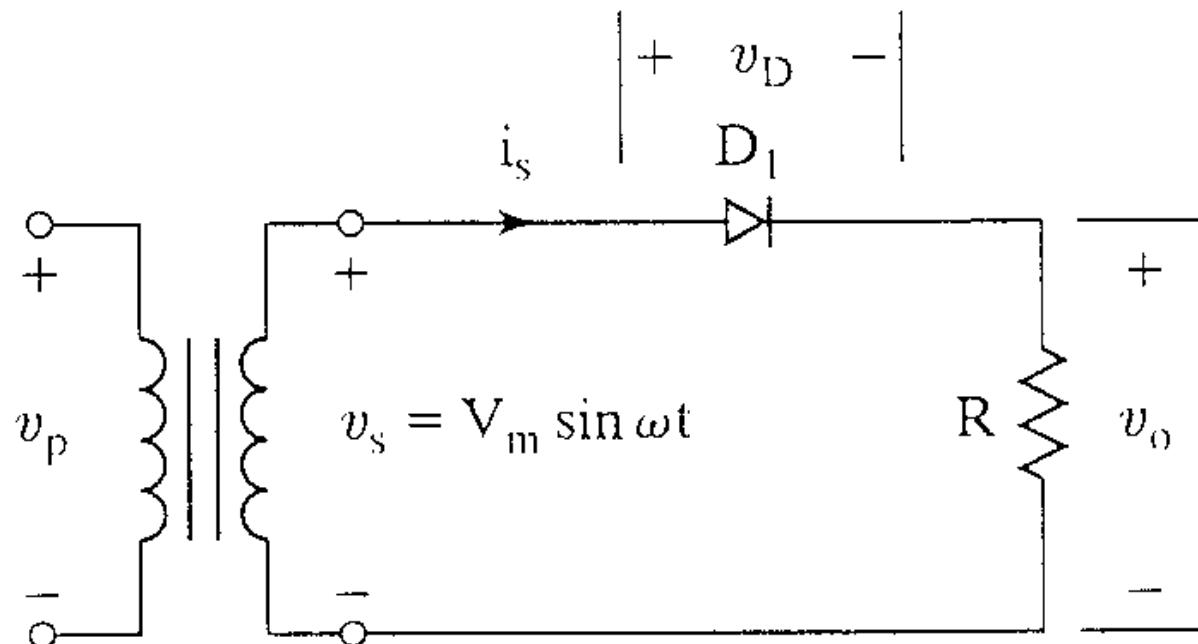


# Performance Parameters

- Average value of the output voltage,  $V_{dc}$
- Average value of the output current,  $I_{dc}$
- Output dc power,  $P_{dc}$ 
  - $P_{dc} = V_{dc} I_{dc}$
- rms value of the output voltage,  $V_{rms}$
- Output ac power,  $P_{ac}$ 
  - $P_{ac} = V_{rms} I_{rms}$

# Example 3.1

- Determine  $V_{dc}$  and  $I_{dc}$

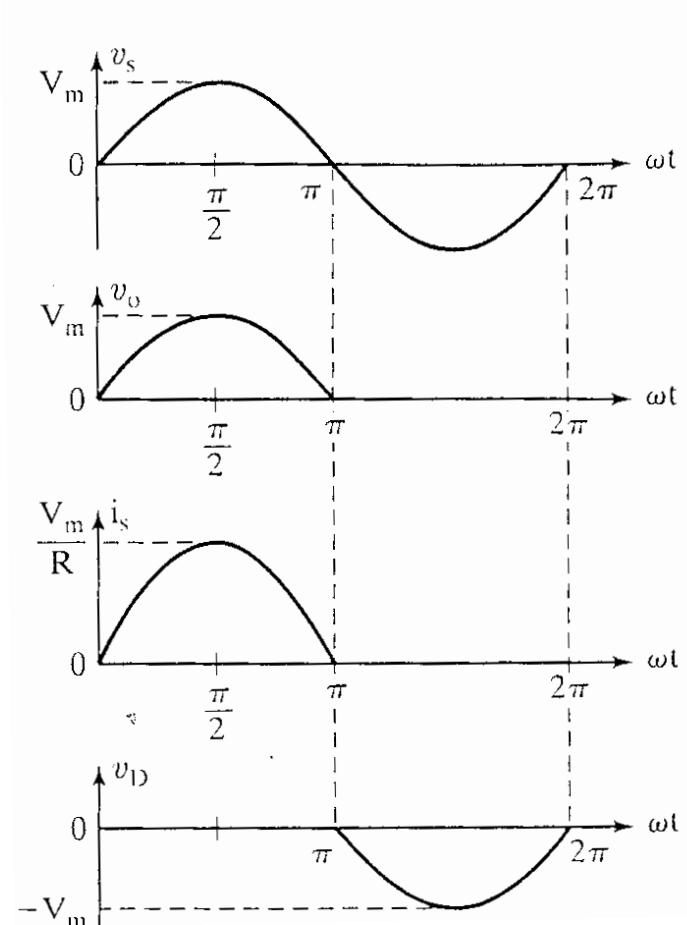


# Determine the Average Voltage, $V_{dc}$

$$V_{dc} = \frac{1}{T} \int_0^T v_L(t) dt$$

$$V_{dc} = \frac{1}{T} \int_0^{\frac{T}{2}} V_m \sin \omega t dt$$

$$V_{dc} = -\frac{V_m}{\omega T} \left( \cos \frac{\omega T}{2} - 1 \right)$$



$$f = \frac{1}{T}$$

$$\omega = 2\pi f$$

$$V_{dc} = \frac{V_m}{\pi} = 0.318 V_m$$

$$I_{dc} = \frac{V_{dc}}{R} = \frac{0.318 V_m}{R}$$

# FULL WAVE RECTIFIER

- Center-Tapped
  - Bridge

# Full-Wave Rectification – circuit with center-tapped transformer

- Positive cycle, D2 off, D1 conducts;

$$V_o - V_s + V_\gamma = 0$$

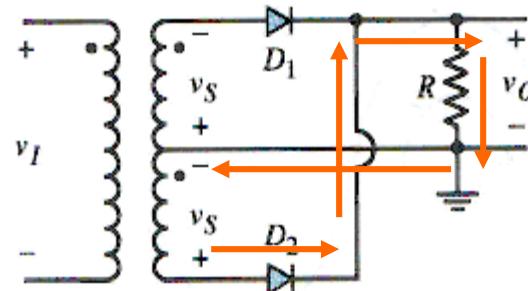
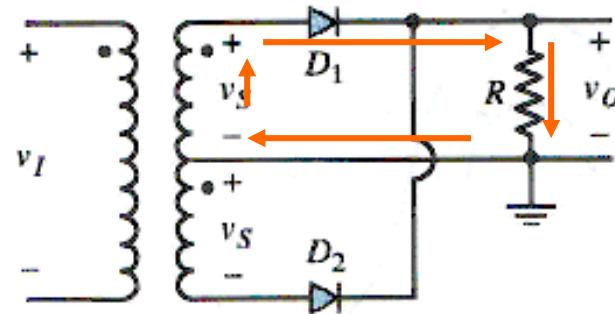
$$\textcolor{red}{V_o = V_s - V_\gamma}$$

- Negative cycle, D1 off, D2 conducts;

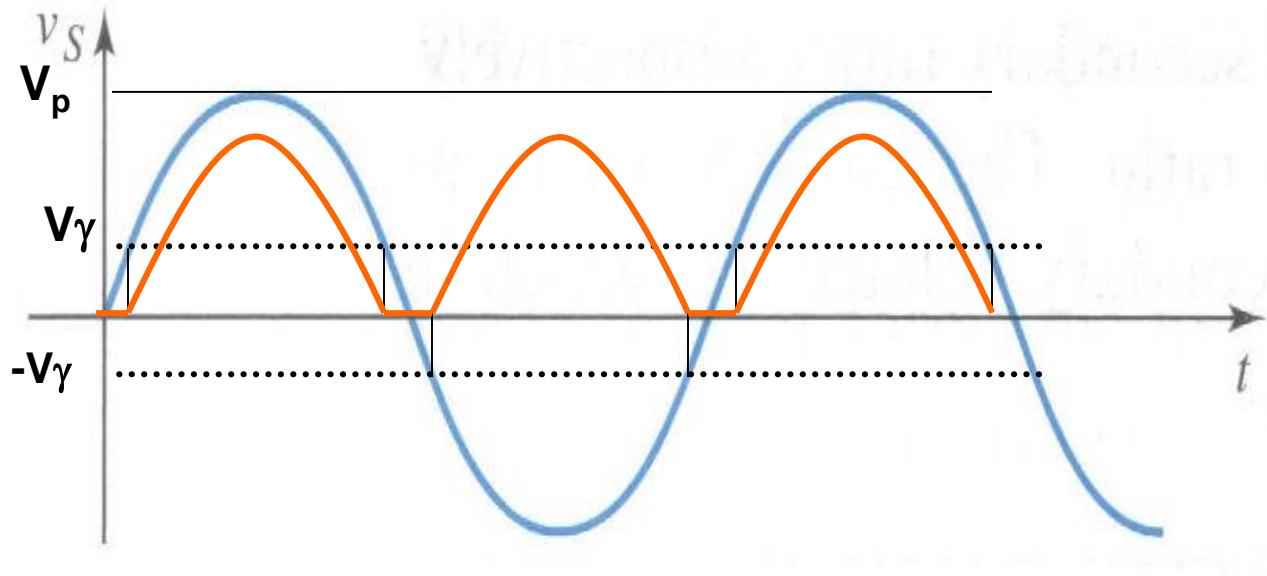
$$V_o - V_s + V_\gamma = 0$$

$$\textcolor{red}{V_o = V_s - V_\gamma}$$

- Since a rectified output voltage occurs during both positive and negative cycles of the input signal, this circuit is called a **full-wave rectifier**.
- Also notice that the **polarity of the output voltage for both cycles is the same**



$$V_s = V_p \sin \omega t$$



Notice again that the peak voltage of  $V_o$  is lower since  $V_o = V_s - V_\gamma$

- $V_s < V_\gamma$ , diode off, **open circuit**, no current flow,  $V_o = 0V$

# Full-Wave Rectification –Bridge Rectifier

- **Positive cycle**,  $D_1$  and  $D_2$  conducts,  $D_3$  and  $D_4$  off;

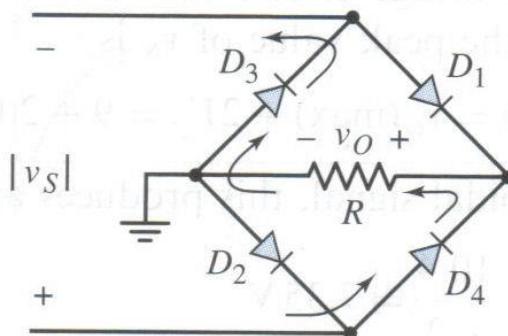
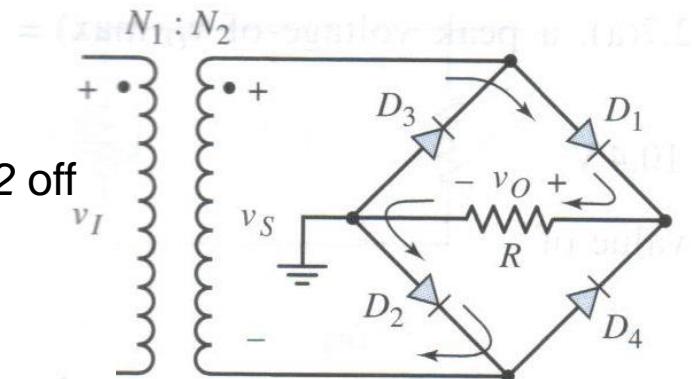
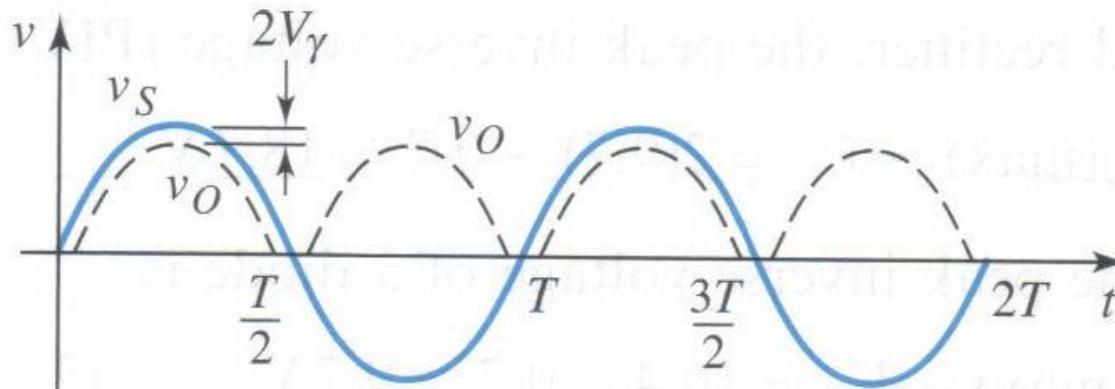
$$+ V_g + V_o + V_g - V_s = 0$$

$$\underline{V_o = V_s - 2V_g}$$

- **Negative cycle**,  $D_3$  and  $D_4$  conducts,  $D_1$  and  $D_2$  off

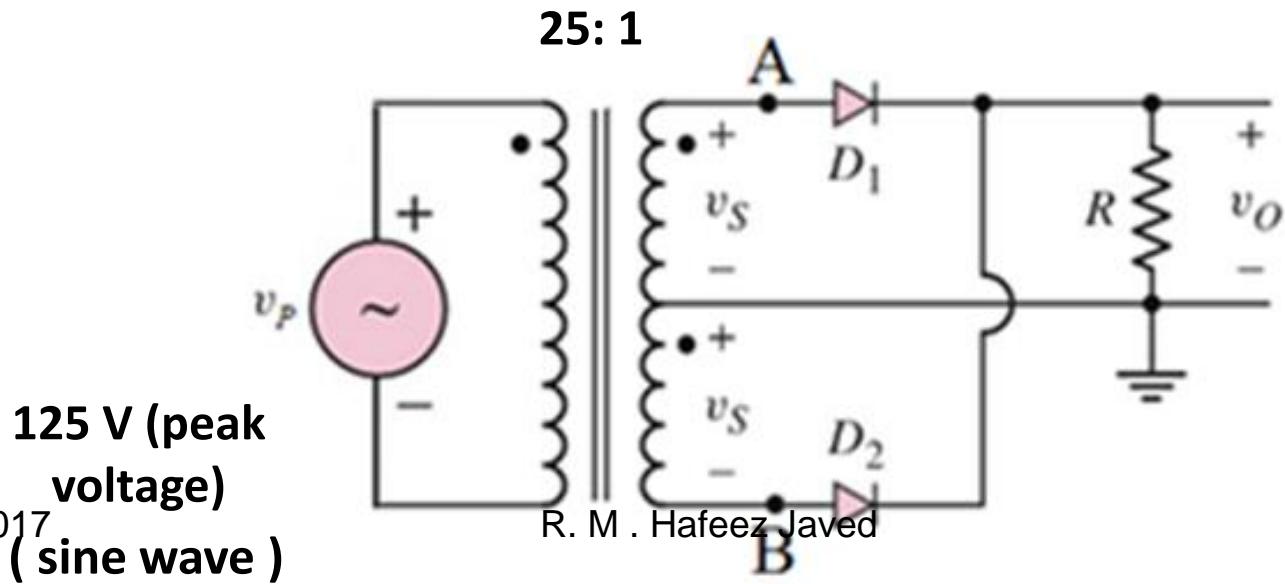
$$+ V_g + V_o + V_g - V_s = 0$$

$$\underline{V_o = V_s - 2V_g}$$



- Also notice that the **polarity of the output voltage for both cycles is the same**

- A full-wave center-tapped rectifier circuit is shown in Fig. 3.1. Assume that for each diode, the cut-in voltage,  $V_g = 0.6V$  and the diode forward resistance,  $r_f$  is  $15\Omega$ . The load resistor,  $R = 95 \Omega$ . Determine:
  - peak output voltage,  $V_o$  across the load,  $R$
  - Sketch the output voltage,  $V_o$  and label its peak value.



- SOLUTION**

- peak output voltage,  $V_o$

$$V_s(\text{peak}) = 125 / 25 = 5\text{V}$$

$$V_\gamma + I_D(15) + I_D(95) - V_{s(\text{peak})} = 0$$

$$I_D = (5 - 0.6) / 110 = 0.04 \text{ A}$$

$$V_o(\text{peak}) = 95 \times 0.04 = \underline{\underline{3.8\text{V}}}$$

